

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database				
	US Patents Full-Text Database				
	US OCR Full-Text Database				
	EPO Abstracts Database				
	JPO Abstracts Database				
	Derwent World Patents Index				
	IBM Technical Disclosure Bulletins				
Term:	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (cache or ram or buffer))				
Display:	<input type="text" value="10"/>	Documents in Display Format:	<input type="text" value="TI"/>	Starting with Number	<input type="text" value="1"/>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image					

Search History

DATE: Monday, August 16, 2004 [Printable Copy](#) [Create Case](#)

Set Name	Query	Hit Count	Set Name result
side by side			
	DB=USPT; PLUR=YES; OP=OR		
L23	L21 and (((pipelin\$3 or stream\$3) near3 (cache or buffer or ram)) same ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3))	4	L23
L22	L21 and ((pipelin\$3 or stream\$3) near3 (cache or buffer or ram))	268	L22
L21	l14 or l8	3069	L21
L20	(5131080 5276851 5287487 5392407 5426739)! [pn]	5	L20
L19	L8 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	3	L19
L18	L1 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	2	L18
L17	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (fifo))	7	L17
L16	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) with (cache or ram or buffer))	72	L16
L15	L14 and (((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3) same (cache or ram or buffer))	97	L15
L14	711/167,168,169.ccls.	1529	L14
L13	l8 and ((ram adj buffer) same (simultaneous\$2 or concurrent\$2))	1	L13
L12	l8 and (ram adj buffer)	11	L12
L11	5559952.pn.	1	L11
L10	L8 and ((disk or drive) same (cache or ram) same ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3))	16	L10

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L9 L8 and ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3)
L8 l1 or 711/131,129.ccls.
L7 L1 and (ram with buffer\$3 with threshold)
L6 l5 and ((simultaneous\$2 or concurrent\$2) with read\$3 with writ\$3)
L5 L4 and l3
L4 L1 and (ram with buffer\$3 with writ\$3)
L3 L1 and (ram with buffer\$3 with read\$3)
L2 L1 and (ram with buffer\$3)
L1 711/111,112.ccls.

150 L9
1646 L8
1 L7
7 L6
35 L5
46 L4
48 L3
114 L2
1256 L1

END OF SEARCH HISTORY

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